

1.0 INTRODUCTION

This document describes the requirements for procuring the unpackaged integrated circuit die used in the manufacture of part number 2847XC which is a Quad 80-Bit Static Shift Register. All changes and/or substitutions to the procured parts are subject to prior approval by ICI. This specification is to be used with GEN-008.

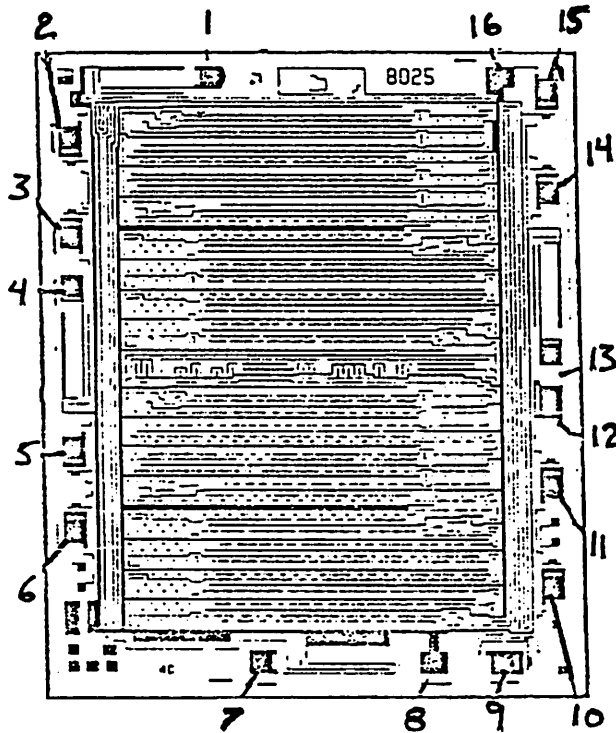
2.0 APPLICABLE DOCUMENTS

GEN-008 General Specification for Integrated Circuit Dice

3.0 REQUIREMENTS

3.1 Mechanical/Physical Characteristics

Each die shall be configured as shown in Figure 1.




NOTES:

- 1. Mfr. Advanced Micro Devices
- 2. Die size .106 x .128 ± .005

FIGURE 1 - DIE CONFIGURATION

99126-003

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|--|-------------------------|---------------------------------|-----------------------------|-----------------|
|  Integrated Circuits Incorporated 10301 WILLOWS ROAD, REDMOND, WA 98052 | SIZE A | CAGE NO. 50821 | DRAWING NO. 85064 | REV A |
| | SCALE | | SHEET 2 OF 3 | |